

## **ABSTRACT**

The present invention describes an integrated image detecting apparatus with low noise, which transforms optical current to voltage and comprises an optical detecting element, an integrated circuit, a correlated double sampling  
5 circuit, and an output circuit. The present invention is a CMOS process and is designed for different CMOS image application systems, which keeps the advantages of low power consumption and better integration. Shifts of circuit characteristics caused by process variation are furthermore eliminated.